

FIG. 1

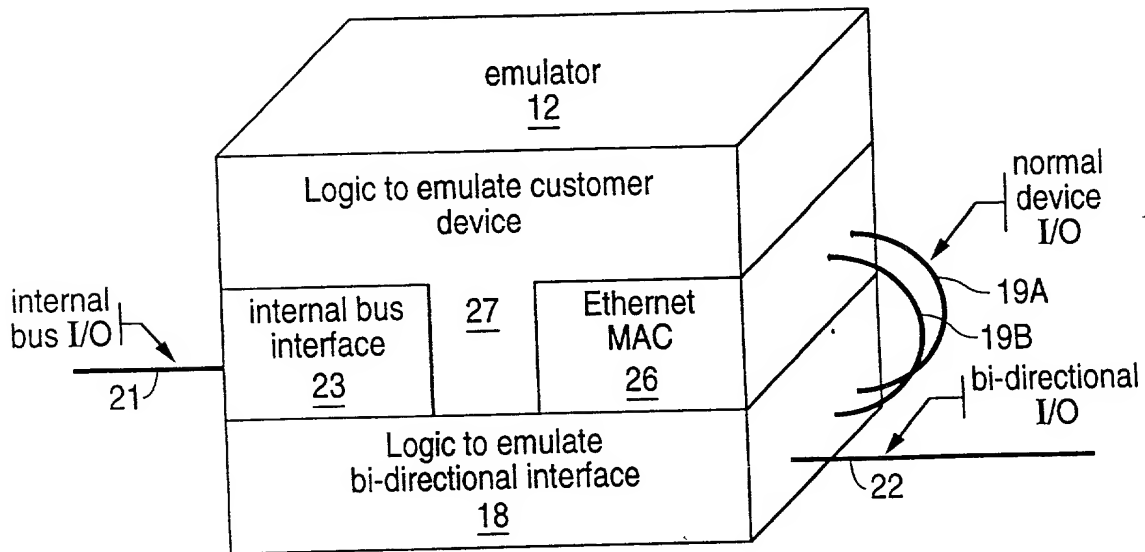


FIG. 2

FIG. 3

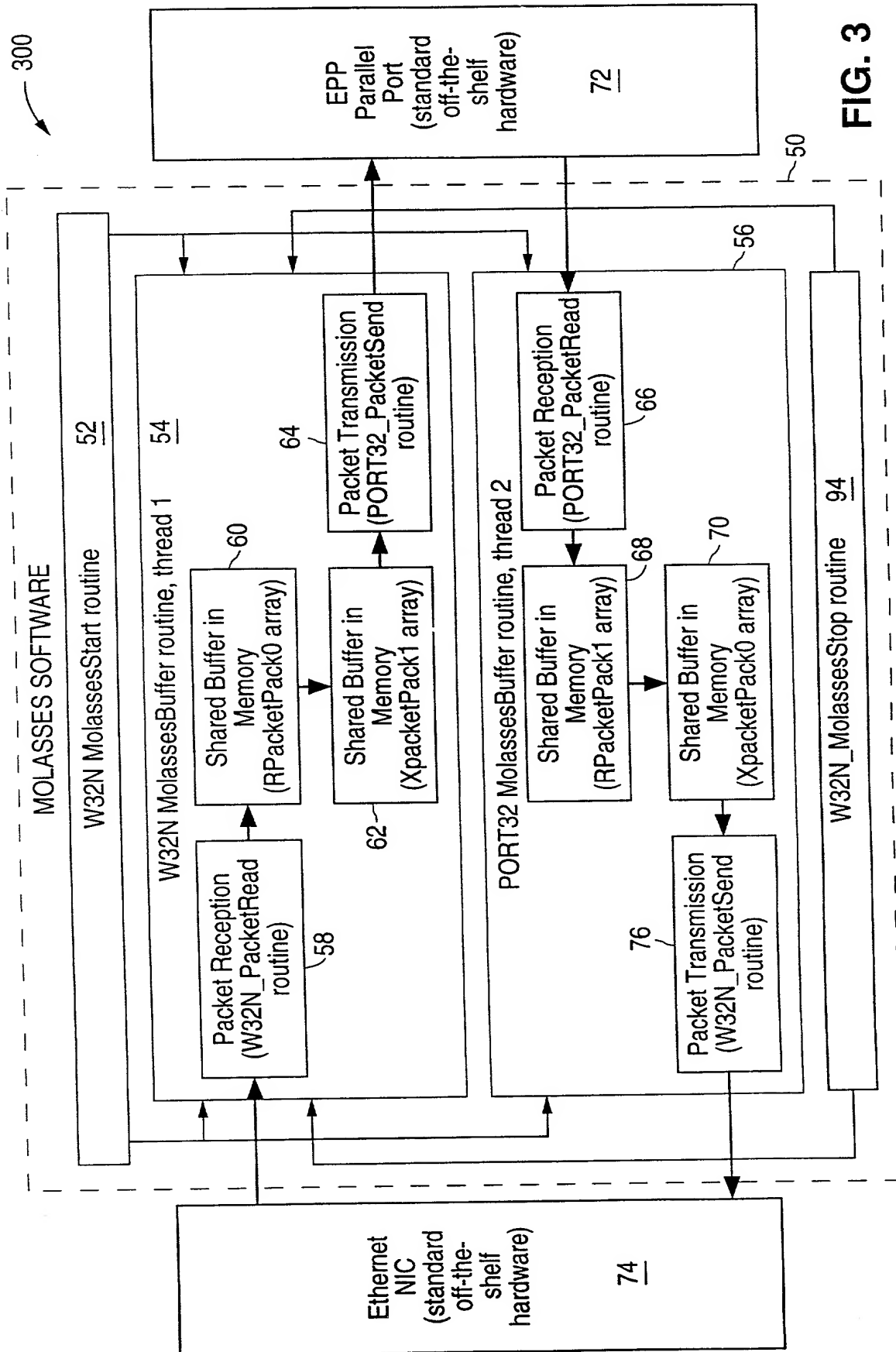


FIG. 3

X

Molasses

File Edit Help

Adapter1: #2 DAVICOM 9102 PCI Fast Ethernet Adapter 82

LPT port addr: 378 84

Log File: 90

Comments: On Off

GO

STOP

86

Verbose mode

88

Silent mode

Status: 92

Error Count: 0

FIG. 4

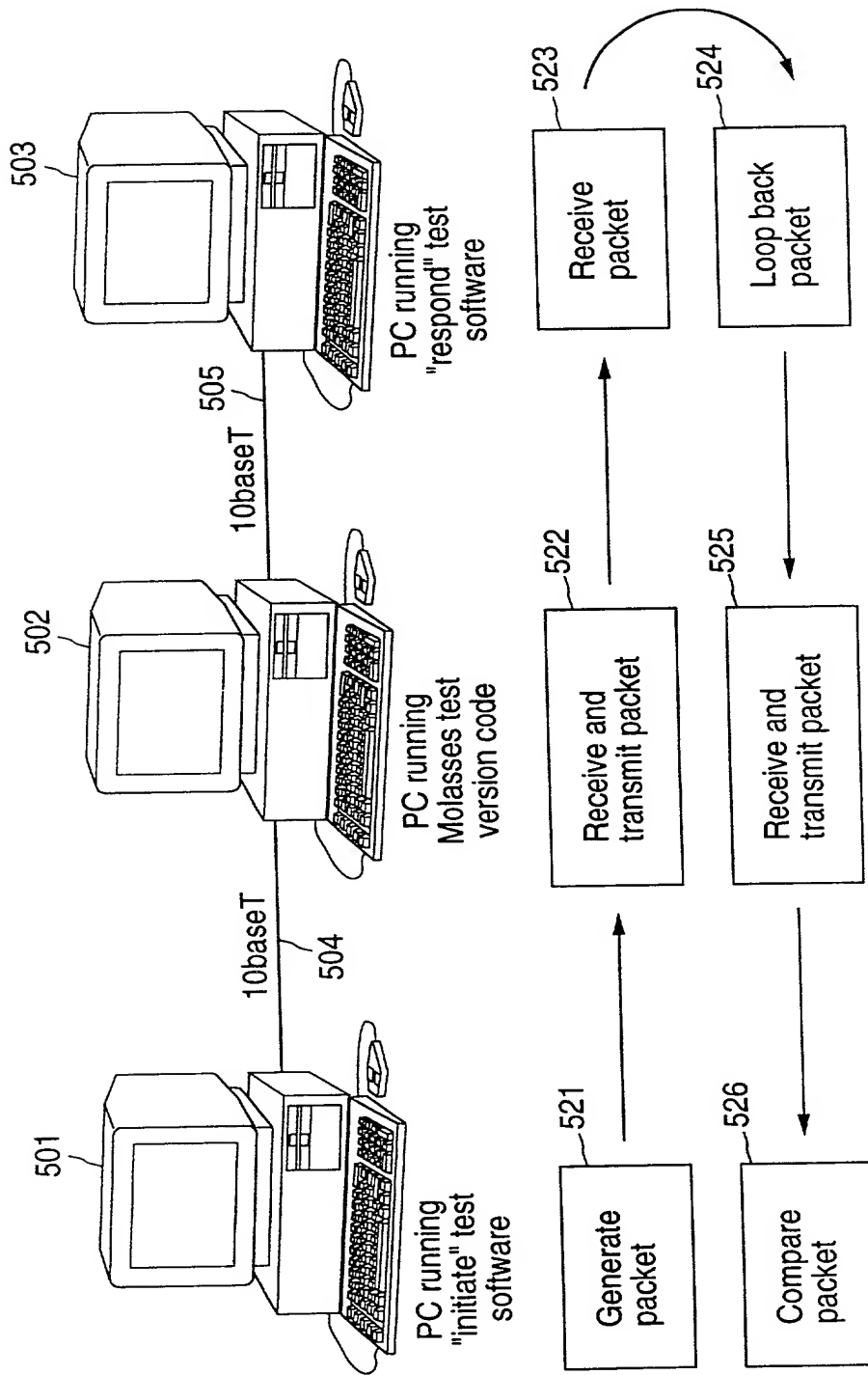


FIG. 5

FIG. 6

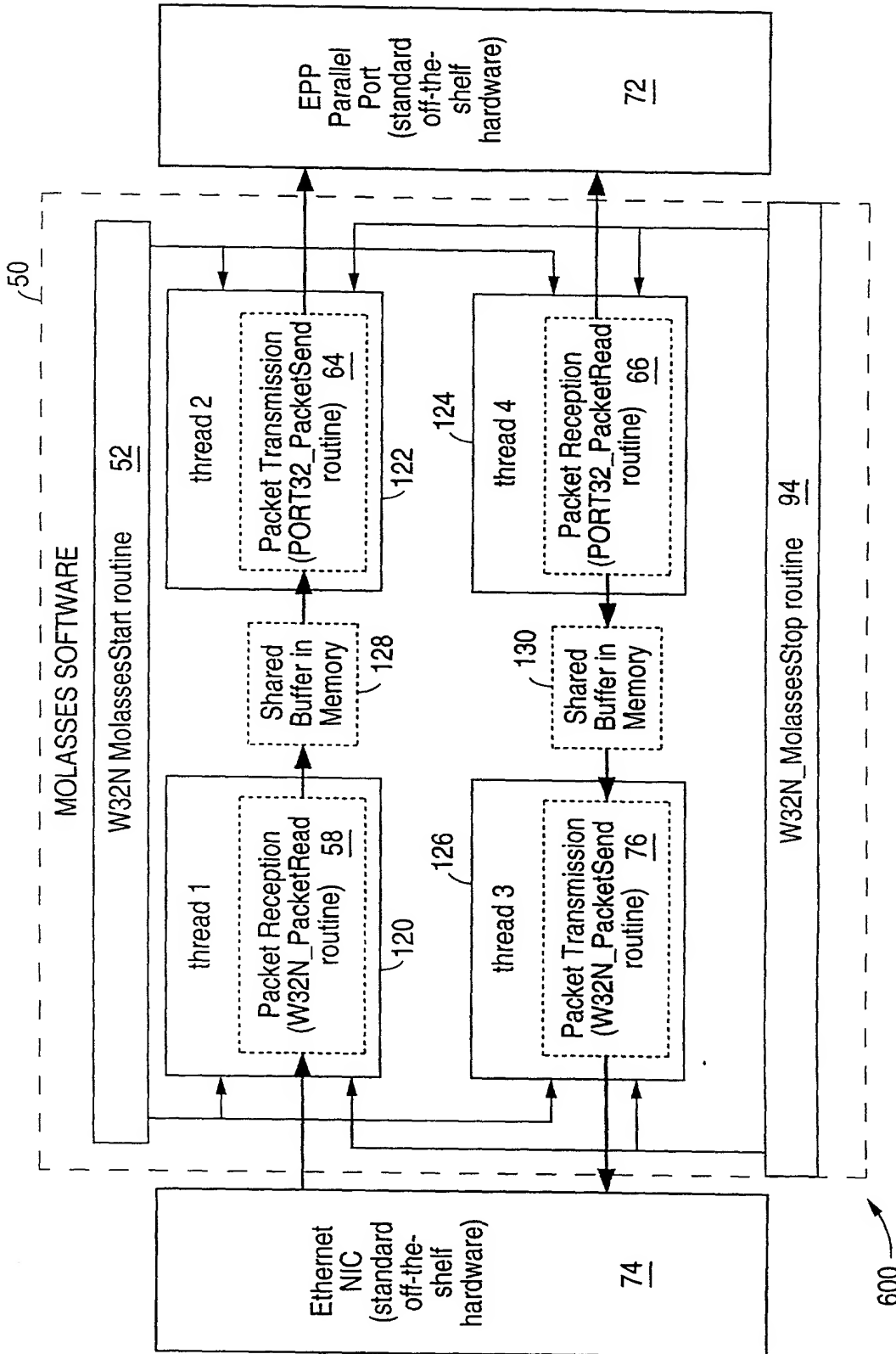


FIG. 6

SYSTEM AND METHOD FOR CONNECTING A LOGIC
CIRCUIT SIMULATION TO A NETWORK

Inventor: Robert M. Zeidman

Attorney Docket No.: M-8637-1P_US

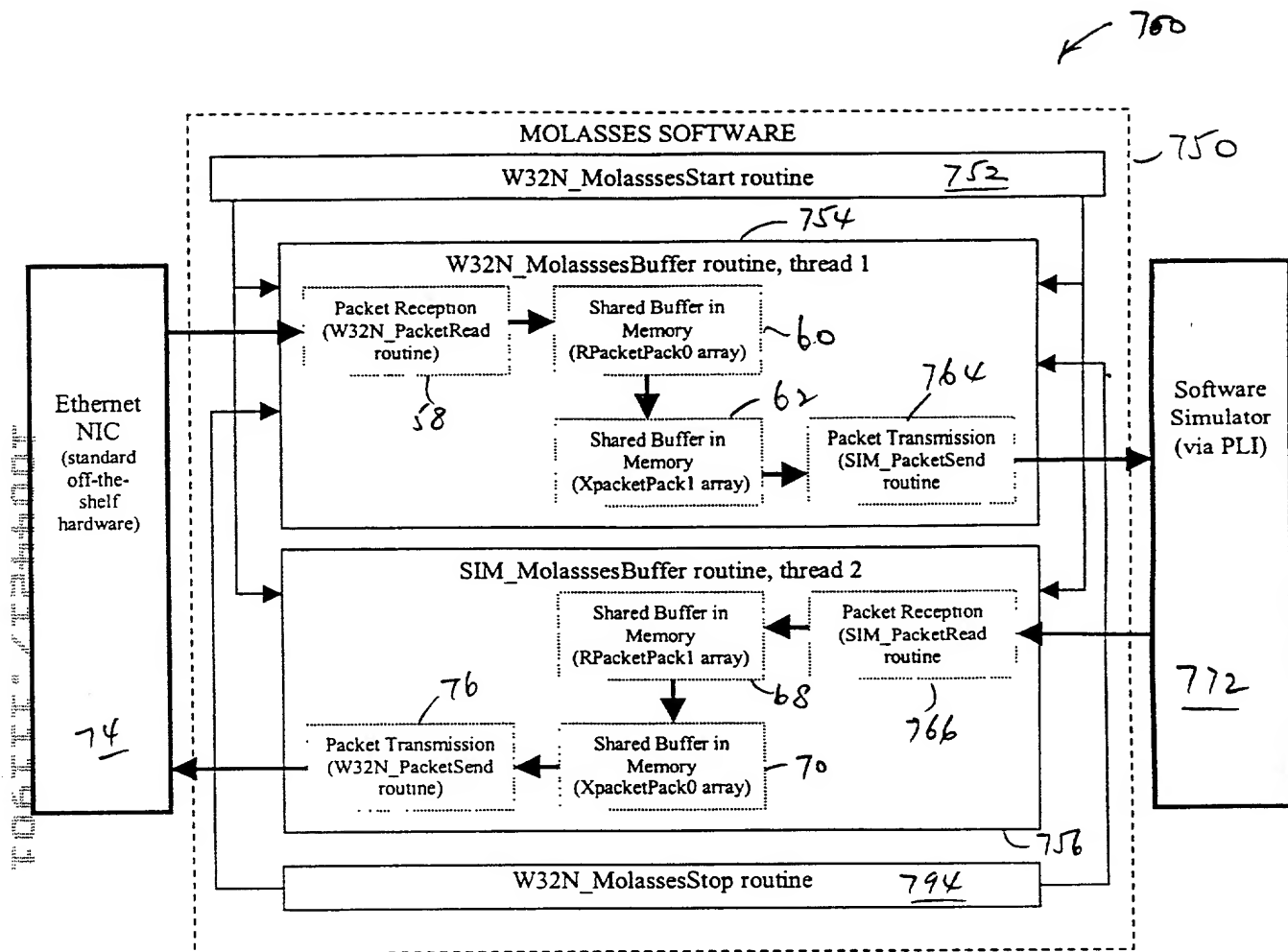
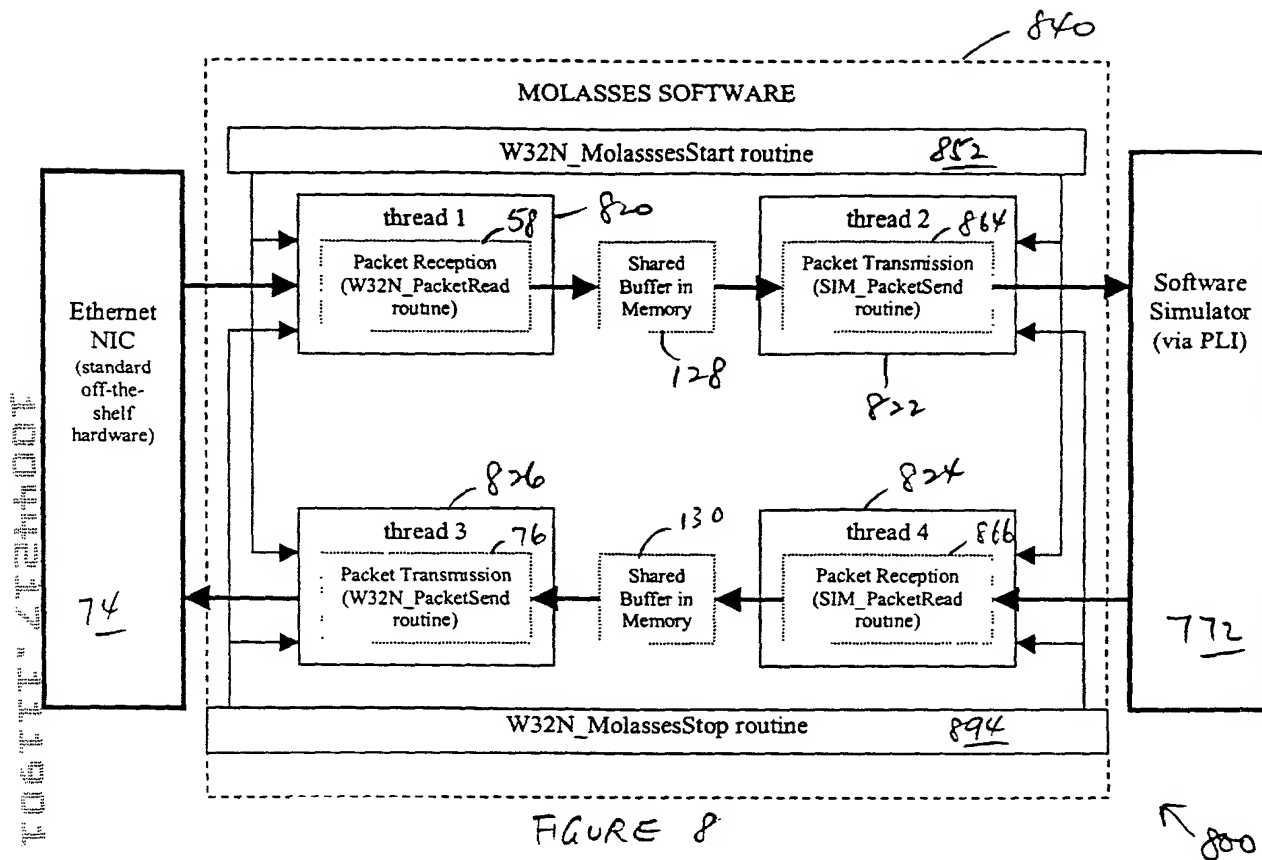


FIGURE 7



SYSTEM AND METHOD FOR CONNECTING A LOGIC
CIRCUIT SIMULATION TO A NETWORK

Inventor: Robert M. Zeidman

Attorney Docket No.: M-8637-1P US

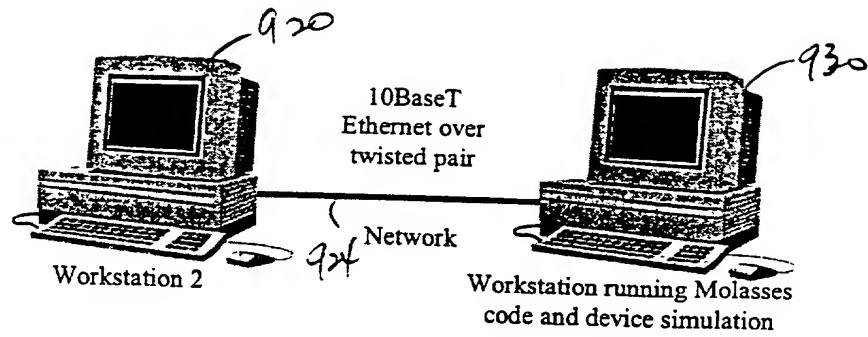


FIGURE 9

10044317.11901
T06117.1244007